

CONDUCTANCE QUANTIZATION IN GOLD NANOWIRES AT LOW TEMPERATURE

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When a scanning tunnelling microscope (STM) tip is driven into a metallic sample surface a nanometer sized wire (nanowire) is formed during the subsequent retraction. The electrical conduction measured during this retraction process shows signs of quantized conductance in units of $2e^2/h$. Due to the inherent non-reproducibility of the measured conductance curves a standard technique is to build histograms from a large number of curves. Such histograms, built with conductance experiments on gold nanowires at room temperature, show 3-4 peaks at integer values of $2e^2/h$, while in a low temperature mechanically controlled break junction study only the first peak is reported. In this work, histograms made up of thousands of consecutive curves at 4K are presented, showing up to 5 conductance peaks. An explanation for this discrepancy could be a higher nanowire temperature resulting from the higher retraction speed used in our measurements. However, a simple estimation, where we used macroscopic heat transport theory, resulted in a very low temperature increase, less than 1 μ K, ruling out this possibility. Thus, no significant difference with previous room temperature studies were observed, pointing to a conductance quantization that is the same at room and low temperature.

1. Introduction

The electrical conductance through a narrow constriction with a diameter of the order of the electron wavelength is quantized in units of $G_0 = 2e^2/h$ [1,2]. Such conductance quantization is observed in semiconductor devices containing a two-dimensional electron gas (2DEG) at low temperature [3,4]. Similar effects are possible [5,6] at room temperature in metallic wires with a diameter of the order of 1 nm (nanowires) and are observed using scanning tunnelling microscopy (STM) [7-11], mechanically controlled break junctions (MCBJ) [12-15], or as recently shown [16,17] just by using plain

household wires. These techniques use the same basic principle: by pressing two metal pieces together a metallic contact is formed which can be stretched to a nanowire by the subsequent separation of the electrodes (see inset in Fig. 1). The conductance in such a system is found to decrease in abrupt steps, of about $2e^2/h$ height, just before the contact breaks. Due to the inherent non-reproducibility of the measured conductance curves, histograms are constructed using a few hundred experimental curves [18]. The scientific relevance of these histograms has been the subject of a recent controversy [18,19]. One interpretation of the stepped behaviour of the conductance is based on conductance quantization. The other view describes the conductance steps as due to the discrete atomic size which gives rise to discrete contact size changes during the nanowire elongation process.

In an MCBJ study by Krans [15] on Au nanowires at 4.2 K no signs of conductance quantization are observed apart from a well-defined conductance peak at $1 G_0$, while at 77 K three conductance peaks are seen. In another MCBJ study, Muller *et al.* [14] show that, at room temperature, conductance quantization is clearly seen and more prominently than in the earlier low temperature measurements. This is in agreement with all STM-experiments on gold at room temperature [7-11]. The explanation [14,15] for this temperature dependency in the MCBJ experiments is that for higher temperature, due to the higher kinetic energy of the atoms, the contact can explore a wider range of structures to find the most favourable atomic configuration giving the wire a more favourable form for quantized conductance to be observed.

To some extent this is also supported by numerical simulations by Bratkovsky *et al.* [20] where the nanowire tends to become more uniform in cross-section and internal defects are seen to more easily heal at high temperatures. However, the relevance to the experiments is not clear since the pulling speed are many orders of magnitude larger in the simulations than in the experiments (3.5 ms^{-1} for simulations, $1 \text{ }\mu\text{ms}^{-1}$ for STM experiments, and 0.01 nms^{-1} for the MCBJ experiments). In contrast to the MCBJ experiments, in a recent STM study by Sirvent *et al.* [21] three clear peaks are resolved at three different temperatures: 4, 77, and 300 K, however not exactly at integer values.

In this report we show that, using an STM at 4 K, up to five peaks at integer values of G_0 could be observed. Furthermore, using a simple estimation based on macroscopic heat transport theory, the higher separation speed in STM as compared to MCBJ, will not rise the temperature significantly, which could have explained the experimental discrepancies.

2. Experimental

The tips were made of 0.25 mm 99.99 % pure gold and the samples were evaporated gold thin films. The measurements were carried out in vacuum using a low temperature STM at 4K. To build the histograms, a triangular waveform was fed to the piezo-actuator driving the tip repeatedly in and out of contact with the sample surface with a typical frequency of 5 Hz and with an amplitude of $1.4 \text{ }\mu\text{m}$, which gives a speed of $14 \text{ }\mu\text{m/s}$. During this procedure the conductance was measured using a current to voltage converter with a gain of 10^5 ($1 \text{ V}/100 \text{ }\mu\text{A}$, $1 \text{ }\mu\text{s}$ rise time, and $3 \text{ }\mu\text{s}$ settling time). A 500

MHz bandwidth and 5 G samples/s, 8-bit, digital oscilloscope (LeCroy 9345AM) was used for real time building of the histograms. The tip-sample bias in all experiments reported here was 90.3 mV corresponding to a quantum current step of $7 \mu\text{A}$ ($2e^2/h$ is about $77.5 \mu\text{S}$). Notice that all the measured curves were used to build the histogram. Only contact breakage experiments were used to construct the histograms. The histograms presented here are smoothed by averaging 3 bins to the side to remove noise. A better way is to compensate for the non-ideal differential linearity (NIDL) [22,23]. Both approaches obtain similar results but the NIDL correction produces sharper peaks.

3. Results

Fig. 1 shows a conductance histogram built with 5000 consecutive indentations experiments using Au electrodes at 4.2 K and with an electrode separation speed of $20 \mu\text{m/s}$. It clearly shows peaks at integer values of $2e^2/h$ up to $n=5$. The values of the conductance are plotted after a subtraction of a series resistance of 390Ω . This has been a common procedure in all quantized conductance reports, and usually has been justified by the resistance of the leads, i.e., what is not the nanowire itself. As shown recently in a theoretical work, this resistance can arise also from disorder within the nanowire [24], and recent experimental results point in this direction [25]. This series resistance varies with experimental conditions, but is usually in the range $200\text{-}1000 \Omega$. Cleaner systems usually gives lower series resistance values. Fig. 2 shows a histogram built with 900 000 consecutive curves with a similar appearance as in Fig. 1. The separation speed was $20 \mu\text{m/s}$ and a series resistance of 390Ω has been subtracted.

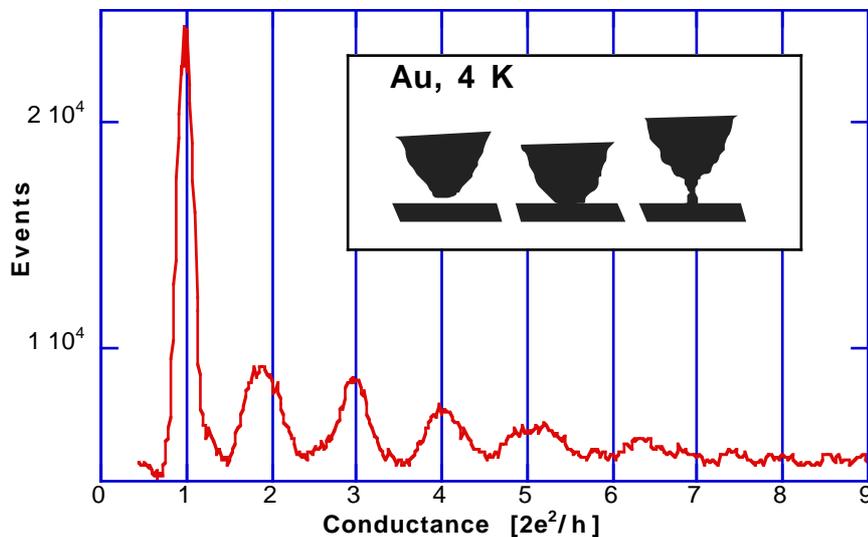


Figure 1. Conductance histogram build with 5000 consecutive curves. Inset shows the principle of nanowire formation. By pressing a tip into the sample and subsequent retracting the tip a short and narrow wire is formed.

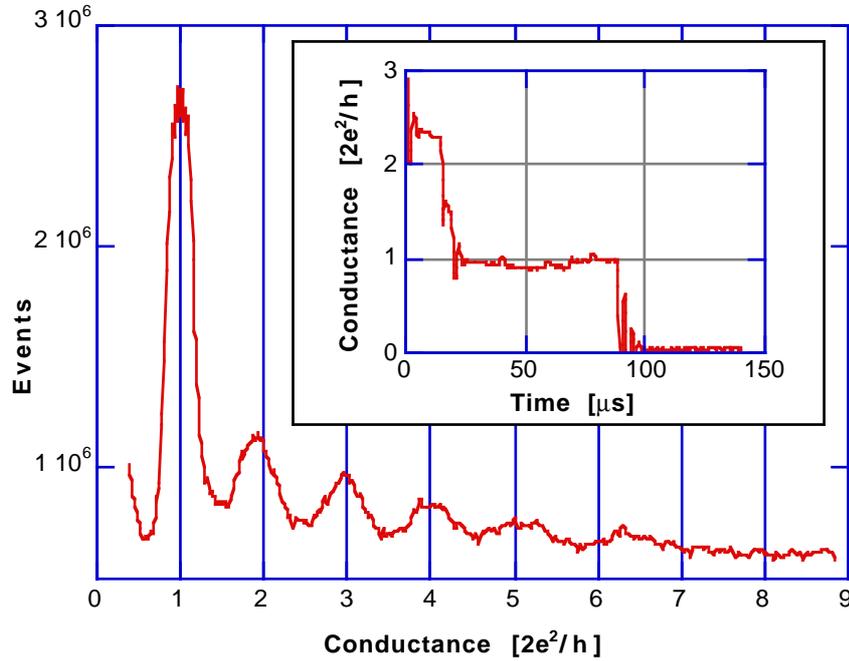


Figure 2. Conductance histogram build with 900000 consecutive curves. Inset shows a typical conductance versus time staircase.

4. Discussion

Notice in the histograms shown in Figs. 1 and 2 that, as the conductance value increases, the peak height decreases and the width increases, in agreement with room temperature STM-experiments [10,17] as well as with theoretical calculations [10]. In our RT studies [16,17] we find exactly this behaviour as well. This is in contrast with MCBJ experiments [14] at RT which show peaks with almost the same height as well as height-width relation. However, the main difference between the present results and the MCBJ results at 4K is that the MCBJ experiments does not show any sign of quantized conductance, apart from a well defined peak at $n=1$. One possible explanation for this discrepancy could be that the temperature in the wire is not the same as in the surrounding bath, due to the higher separation speed used in our STM-experiments. In the MCBJ experiments the separation speed is orders of magnitude lower than in our case (0.1 nm/s and 10000 nm/s respectively). If the energy released during the elongation process is transformed to heat, and this excessive heat is not transported away rapidly enough the temperature in the wire will be much higher than the surrounding bath. Higher temperature will give a more favourable wire, from a quantized conductance point of view, as discussed by Muller *et al.* [14] and in the theoretical work by Bratkovsky *et al.* [20]. They show that at high temperatures the nanowire tends to become more uniform in cross section and internal structure defects heal more rapidly.

A simple estimation of the temperature increase could be obtained if one consider the amount of heat that is released during the pulling of the wire. Fig. 2 inset shows a typically conductance versus time graph. The typical life time of a conductance plateau is about 100 μ s. During the pulling process the cross-sectional area decreases, the surface area increases, and energy is released as the binding energies of the surface atoms are smaller than the bulk ones. If the cross-sectional area, A , of the wire is reduced by a factor of 2 the surface area will increase by a factor of 2 under constant volume conditions. If the wire is a cylinder with a diameter of 1 nm and 2 nm length, the initial surface area will change from $6.3 \cdot 10^{-18} \text{ m}^2$ to $12.6 \cdot 10^{-18} \text{ m}^2$. Considering a typical area of an atom of $(2 \cdot 10^{-10})^2 \text{ m}^2$, and a typical difference between binding energy in the surface and in the bulk of 1 eV per atom, a total energy of 160 eV will be released during such an elongation. If this 160 eV is released during the last plateau duration, 100 μ s, the power generated will be about 0.3 pW. One may get a simple estimation of the temperature increase in the nanowire using

$$\Delta T = \dot{Q}L/A\kappa \text{ [K]}, \quad (1)$$

where $\dot{Q} = 0.3 \text{ pW}$ is the power dissipated, L is the length of the wire, and A the cross-sectional area (10^{-18} m^2). Making the assumption that the thermal conductivity, κ , is $1000 \text{ Wm}^{-1}\text{K}^{-1}$, which is typical for high purity Au at 4K, the temperature increase is less than 1 μ K. Even if κ or the dissipation time are decreased orders of magnitude this estimation will still produce a very low temperature increase in the nanowire. In our rough estimate we assume that the Joule dissipation is damped in the electrode regions and not in the nanowire.

5. Conclusion

A conductance histogram with 5 clear peaks at integer values of $2e^2/h$ are observed. The height of the conductance peaks is decreasing and the width of the peaks is increasing with increasing n , much in agreement with room temperature STM-experiments [10,17] as well as with theoretical calculations [10]. It has been argued that the higher speed used in STM experiments as compared to MCBJ experiments might result in a higher temperature. However, a simple estimation, based on macroscopic heat transport, produces a temperature rise in the nanowire of the order of μ K, ruling out this explanation. Therefore, conductance quantization in gold nanowires is basically the same at high and low temperatures.

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